

# ABSTRACT OF THE DISCLOSURE

In the present invention, a BTL circuit formed in an IC is constituted in such a manner that a second output stage amplifier receives an output of a first output stage amplifier and generates an inverted output and a switch circuit is provided between any one of the outputs of the first and second output stage amplifier and a speaker, wherein through effecting a muting by making use of a delay time of BTL operation of the second output stage amplifier or alternatively, by making use of switches of respective push-pull structured transistors in place of the switch circuit, an early muting from the time of power source turning ON can be realized.